

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES**

In re Application of: Group Art Unit: 2894

Application No. 10/722,597 : Examiner: Monica LEWIS

Filed: November 28, 2003 : Confirm. No: 8275

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND SEMICONDUCTOR
INTEGRATED CIRCUIT CHIP THEREOF

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Mail Stop: APPEALS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 15, 2009

Sir:

In accordance with the provisions of 37 C.F.R. 41.31, Applicants hereby appeal from the rejection of claims 4-14 and 21-24, as set forth in the Final Office Action mailed December 15, 2008.

A Petition for a three-month extension of time, and the requisite fee (\$1,110.00), is enclosed herewith.

Also enclosed is a credit card authorization form, Form PTO-2038, authorizing the additional amount of \$540.00 to be paid for the requisite Appeal Fee.

Respectfully Submitted,

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